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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Roman HAMERSKI et al.

Serial No. 09/923,647

Filed: August 7, 2001

For: APPARATUS AND METHOD)
FOR FABRICATING A HIGH)
REVERSE VOLTAGE)
SEMICONDUCTOR)

Atty Docket No. 12263-15

Group Art Unit: 2822

Examiner: Ida M. Soward

Customer No.: 27526

PATENT

AUG 20 2002

TECHNOLOGY CENTER 2800

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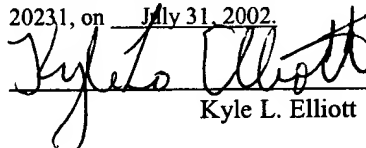
Andt/
Electron
8/22/02
Awa

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail,
with sufficient postage, in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C.
20231, on July 31, 2002.


Kyle L. Elliott

RESPONSE TO RESTRICTION REQUIREMENT

Please cancel claims 16-20 without prejudice.

REMARKS

In response to the Office Action with Restriction Requirement dated July 15, 2002,
Applicant hereby elects, without traverse, Group I, Claims 1-15 and 21 (drawn to an electrical
semiconductor device classified in class 257, subclass 622), for prosecution at this time.

This response does not present any new matter. Accordingly, as all requirements of the
Action have been complied with, an action on the merits and a Notice of Allowance are hereby



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Restriction Requirement

Inventor: Roman HAMERSKI et al.

S.N. 09/923,647

respectfully solicited. Any fees that may be due can be charged to our deposit account number 11-0160.

If any further matter remains to be addressed which may be readily attended to, a telephone call by the Examiner to the undersigned is requested.

Respectfully submitted,

Date: July 31, 2002

Kyle L. Elliott

Reg. No.: 39,485

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ATTORNEYS FOR APPLICANT

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